

## WEST Search History

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DATE: Wednesday, March 17, 2004

<u>Hide?</u>	<u>Set</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L21	l19 and l1 and l2	2
<input type="checkbox"/>	L20	l13 and L19	0
<input type="checkbox"/>	L19	370/389.ccls. or 370/405.ccls or 370/4\$\$.ccls	964
<input type="checkbox"/>	L18	l6 and L17	12
<input type="checkbox"/>	L17	(compar\$4 or detect\$4) same chang\$	366407
<input type="checkbox"/>	L16	6633573.pn.	1
<input type="checkbox"/>	L15	protect\$4 and L14	4
<input type="checkbox"/>	L14	switch\$4 and L13	5
<input type="checkbox"/>	L13	(interrupt adj signal) and L12	5
<input type="checkbox"/>	L12	type\$ and L11	6
<input type="checkbox"/>	L11	l6 and L9	6
<input type="checkbox"/>	L10	l6 and L9	6
<input type="checkbox"/>	L9	(fpga or field programmable gate array) and interval\$ and L7	6
<input type="checkbox"/>	L8	(fpga or field programmable gate array) and L7	6
<input type="checkbox"/>	L7	interrupt\$4 and L6	26
<input type="checkbox"/>	L6	l1 and L5	26
<input type="checkbox"/>	L5	(backplane or back-plane) and L4	26
<input type="checkbox"/>	L4	serial\$4 and L3	32
<input type="checkbox"/>	L3	(upstream or up-stream or up stream) and (down stream or downstream or down-stream) and L2	34
<input type="checkbox"/>	L2	((data adj frame\$) same signal ) and L1	92
<input type="checkbox"/>	L1	(sonet or synchronous optical network) and (ram or random access memory)	883

END OF SEARCH HISTORY